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Patent

CP2133

#6

Attorney's Docket No.: 4640.P006

Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of:

Yervant Zorian

Examiner: Not Yet Assigned

Application No.: 10/083,241

Art Group: 2133

Filed: February 25, 2002

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Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of

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 X 37 C.F.R. §1.97(b).

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 A statement pursuant to 37 C.F.R. §1.97(e) or

 A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

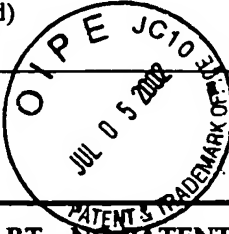
Dated: 6-28, 2002



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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 4640.P006	Application Number: 10/083,241	
		First Named Inventor: Yervant Zorian		
		Filing Date: February 25, 2002		
OTHER ART - NO PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²	
		S. SHOUKOURIAN, V. VARDANIAN, Y. ZORIAN, "An Approach for Evaluation of Redundancy Analysis Algorithms", The Proceedings of IEEE Int. Workshop on Memory Technology, Design and Testing, 2001.		
		D. K. BHAVSAR, "An Algorithm for Row-Column Self-Repair of RAMs and its Implementation in the Alpha 21264", Proc. ITC'1999, pp. 311-318.		
		T. KAWAGOE, J. OHTANI, M. NIRO et al, "A Built-In Self-Repair Analyzer (CRESTA) for embedded DRAMs", Proc. ITC'2000, pp. 567-574.		
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